

DOCKET NO. A1WI2376US

EXPRESS MAIL LABEL NO. EV229159350US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Douglas W. Babcock, Robert A. Duris, and Bruce Hecht

Title: AUTOMATIC TEST EQUIPMENT PIN CHANNEL WITH T-COIL COMPENSATION

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

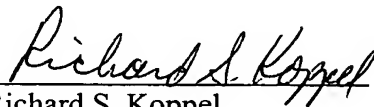
INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is PTO Form 1449 (and copies references articles) for this application. The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 C.F.R. 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

Respectfully submitted,



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FORM PTO-1449 (Modified) INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)	Docket No. A1WI2376US	Application Number
	Applicants: Douglas W. Babcock, Robert A. Duris, Bruce Hecht	
	Filing Date	Group Art Unit

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number							Date	Name	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

	Document Number							Date	Country	Class	Subclass	Translation	
												Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	1.	Three technologies on one chip make a broadband amplifier + "T" coil, by John Addis, Tektronix Inc., Beaverton, Ore., Electronics/June 5, 1972
	2.	Modified T-coil Network, Jim Hagerman, Hughes-JVC Technology Corp., 2310 Camino Vida Roble, Carlsbad, CA 92009; Internet: 72230.1704@compuserve.com, 9/12/95
	3.	Small-Signal MMIC Amplifiers with Bridged T-Coil Matching Networks, L. Selmi, D.B Estreich, and B. Ricco, IEEE Journal of Solid-State Circuits, Vol. 27, No. 7, July 1992, 0018-9200/92\$03.00 [©] 1992 IEEE
	4.	Good Engineering and Fast Vertical Amplifiers, John Addis, Analog Circuit Design, Butterworth-Heinemann, pages 107-122, 1991.
Examiner		Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		